

Profile

| Name: Vinay.T.R | |  | | |
|---|------------------------------|---|--------------|----------------|
| Qualification: B. E, M.Tech | | | | |
| Designation: Assistant Professor | | | | |
| Date of Birth: 08/08/1984 | | | | |
| Date of Joining Institution: 27/01/2010 | | | | |
| <u>Address:</u> | | | | |
| Residence: #65, 4 th Main Road, P & T Colony, Sanjay Nagar, Bangalore - 560094 | | Office: Assistant Professor Computer Science & Engineering Department. Sri Venkateshwara College of Engineering Vidyanagar, Bangalore - 562 157 Off: 080 - 32022690 | | |
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| <u>Details of Educational Qualification:</u> | | | | |
| Degree | Year of completion | Institution | University | Specialization |
| Ph. D | | | | |
| M. Tech / M.S | 2011 | BIT, B'lore | VTU, Belgavi | CSE |
| B.E / B. Tech | 2006 | NCET,B'lore | VTU,Belgavi | CSE |
| <u>Professional Experience:</u> | | | | |
| Teaching Experience | Industrial Experience | Research Experience | | |
| 6.5 Years | 1.5 Years | - | | |

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| <u>Publications:</u> | <ol style="list-style-type: none"> 1. International Journals: 01 2. National Journals: 01 3. International Conferences: - 4. National Conferences: 08 |
| <u>Professional Bodies:</u> | <ol style="list-style-type: none"> 1. <i>Associate Member in MIE: AM158550-1</i> 2. <i>LMISTE : LM 102282</i> |

Name of the Conferences / workshops /Short Term Courses Organized & Attended:

1. Workshop on Intellectual Property Rights – Patents held at IISc, Bangalore on 17th Feb 2016.
2. FDP on Parallel Processing held at MSRIT, Bangalore during 13-15th Mar 2013.
3. Short Term Course on Data Mining conducted by CCE-IISc, Bangalore during August-Dec 2011 with grade 'B'

Publications: (Both National & International):

| Conference / Seminar / W/s | Name, title of the paper, Conference / Seminar, venue and date |
|----------------------------|---|
| 2014-15 | "A Real time Map reduce memory cluster", SVCE-3 rd NCRASET-15, 8 th May 2015 |
| | "Language Agnostic News Summarization Technique" SVCE-3 rd NCRASET-15, 8 th May 2015 |
| | "Methodology to convert serial program to parallel program multicore system" SVCE-3 rd NCRASET-15, 8 th May 2015 |
| | "AutoLocate" under Mobility Section –INFORMATION WEEK Magazine , pg. No. 48, September 2014 issue |
| 2013-14 | "AUTOLOCATE – Mobile App." SVCE-2 nd NCRASET-14, Bangalore 23 rd May-2014 |
| | "To Ascertain Accuracy & Efficiency of Clustering Algorithm for a Common Data Set" SVCE-NCRASET-14, Bangalore 23 rd May-2014 |

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| | “An Ameliorated Methodology for control and data flow of a legacy program for multi core Architecture”, MSRIT-NCSE-14, Bangalore 20-21 st Feb 2014 |
| | “Analysis of Control Flow Graph And its Usage in Program Slicing” NCST-PESIT, Bangalore, 25-26 th Oct. 2013. |
| 2012-13 | “Modified Secondary Key Indexing in FS”, NCNTMS-2013, SVCE Bangalore, 28 Jan-2013. |
| 2011-12 | --- |
| 2010-11 | Vinay T R, “Abstracting Parallel Threads for a Multicore Architecture”, CITAM-2011, CIT Bangalore, 2-3 Dec-2011. |
| | Vinay T R, Pavan Shetty, et.all, Abstracting Parallel Threads from Legacy programs, Conference, SJBIT, Bangalore. |